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(54) **CURRENT CONTROL CIRCUITRY**

(75) Inventor: **Matthew S. Taubman**, Richland, WA (US)

(73) Assignee: **Battelle Memorial Institute**, Richland, WA (US)

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(51) **Int. Cl.**⁷ **G06G 7/12**

(52) **U.S. Cl.** **327/563; 327/562**

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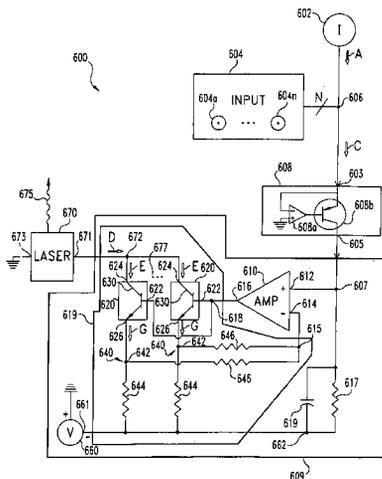
Assistant Examiner—Hai L. Nguyen

(74) *Attorney, Agent, or Firm*—Woodard, Emhardt, Moriarty, McNett & Henry LLP

(57) **ABSTRACT**

Among the embodiments of the present invention is an apparatus that includes a transistor (30), a servo device (40), and a current source (50). The servo device (40) is operable to provide a common base mode of operation of the transistor (30) by maintaining an approximately constant voltage level at the transistor base (32b). The current source (150) is operable to provide a bias current to the transistor (30). A first device (24) provides an input signal to an electrical node (70) positioned between the emitter (32e) of the transistor (30) and the current source (50). A second device (26) receives an output signal from the collector (32c) of the transistor (30).

11 Claims, 6 Drawing Sheets



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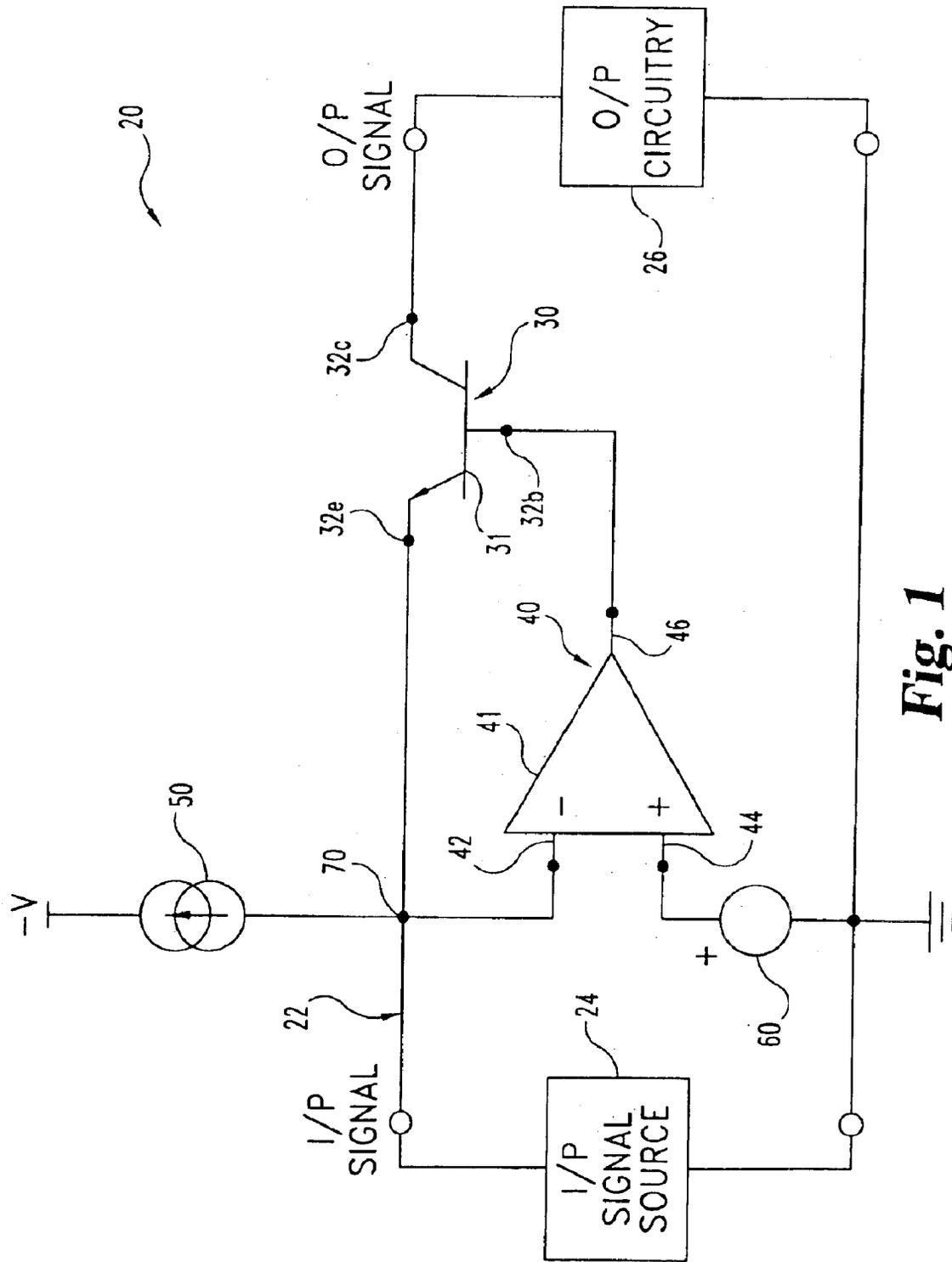


Fig. 1

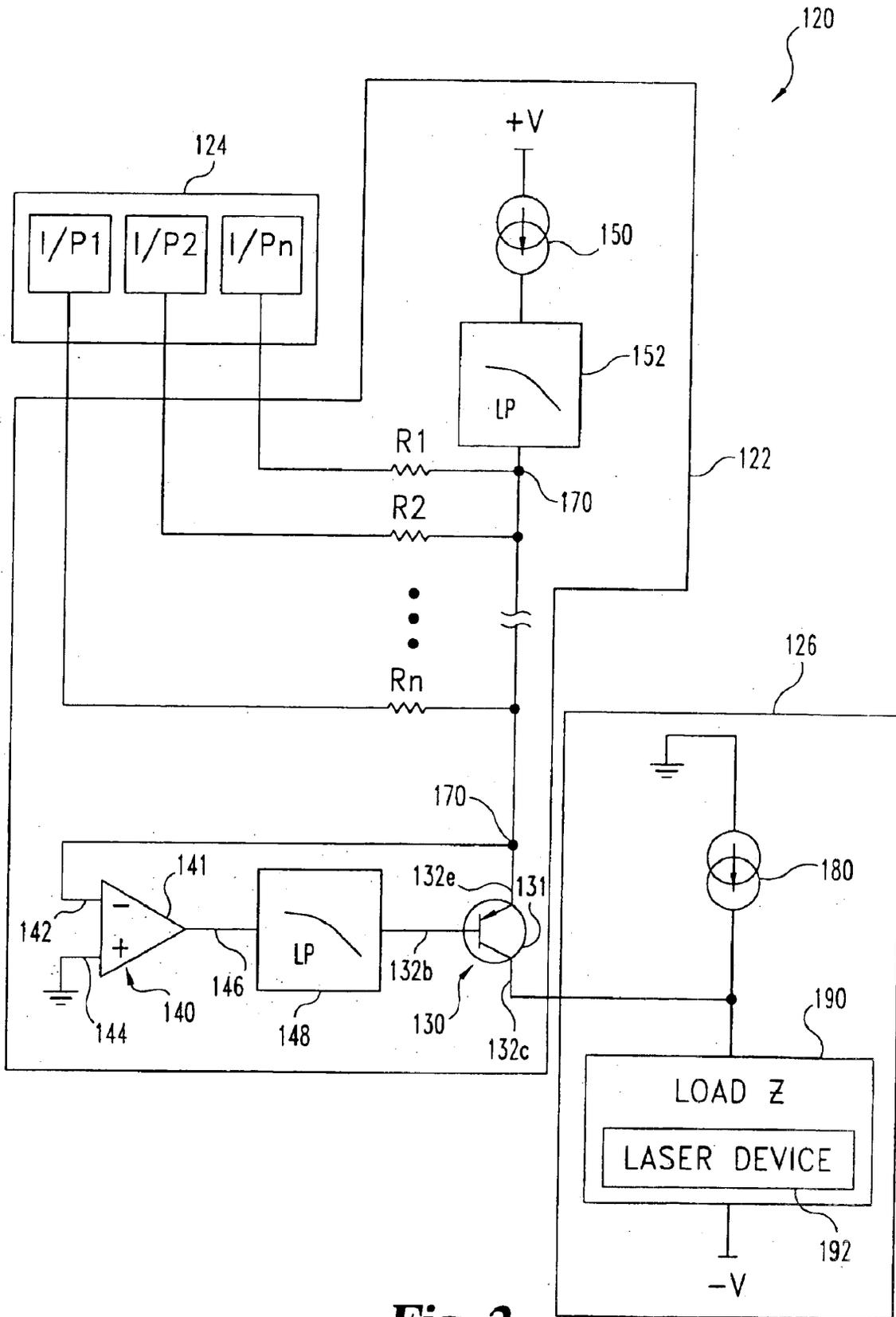


Fig. 2

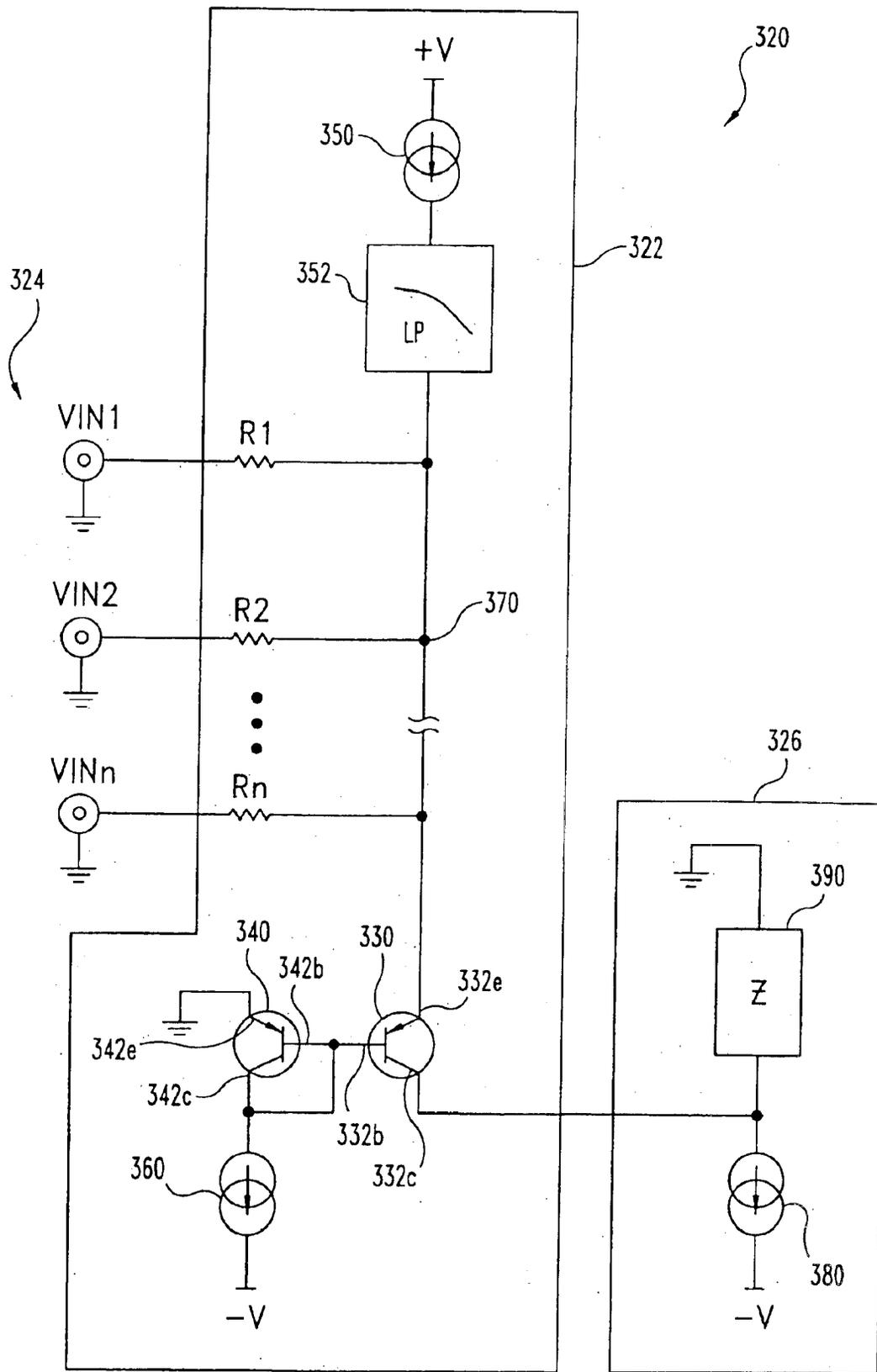


Fig. 4

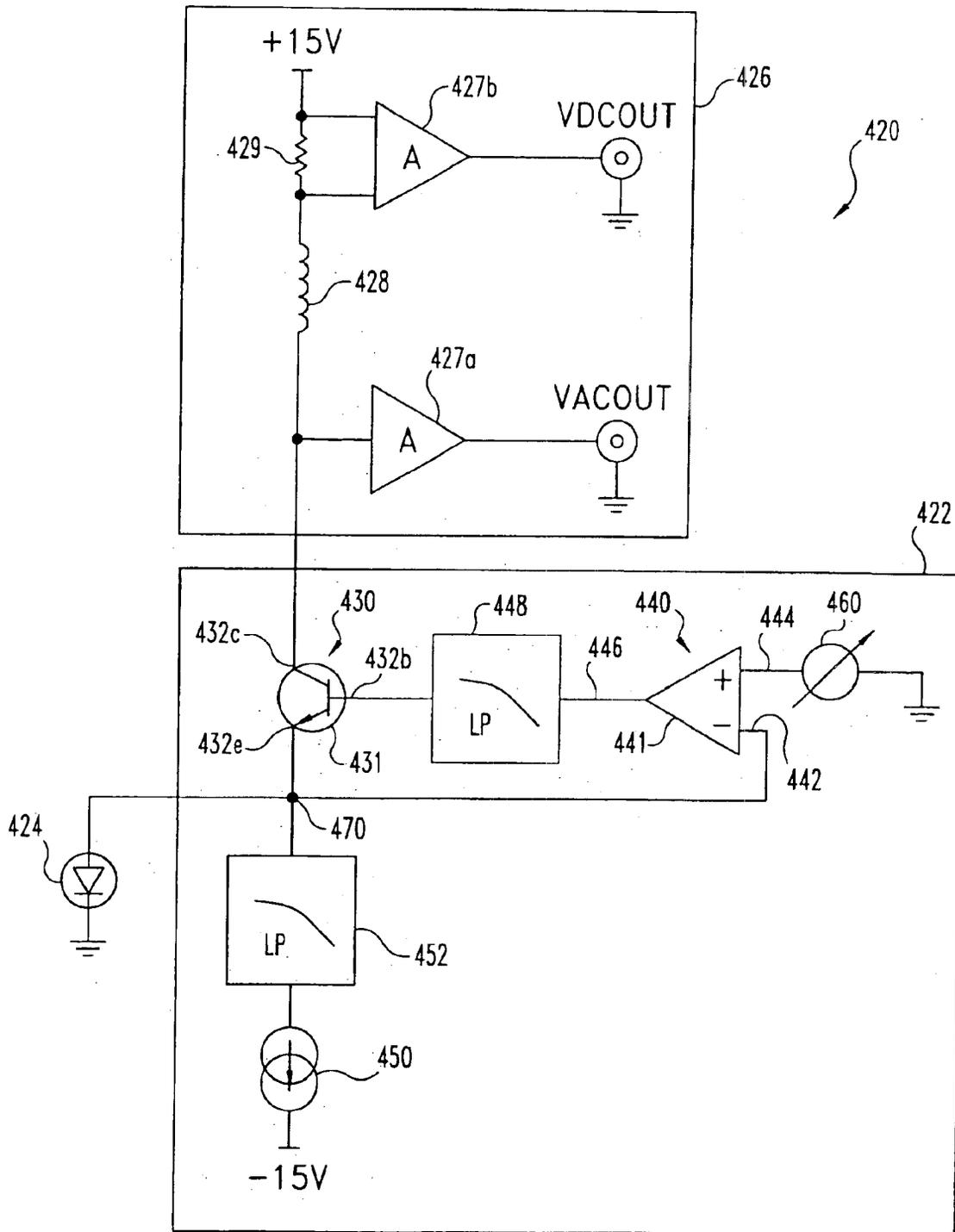


Fig. 5

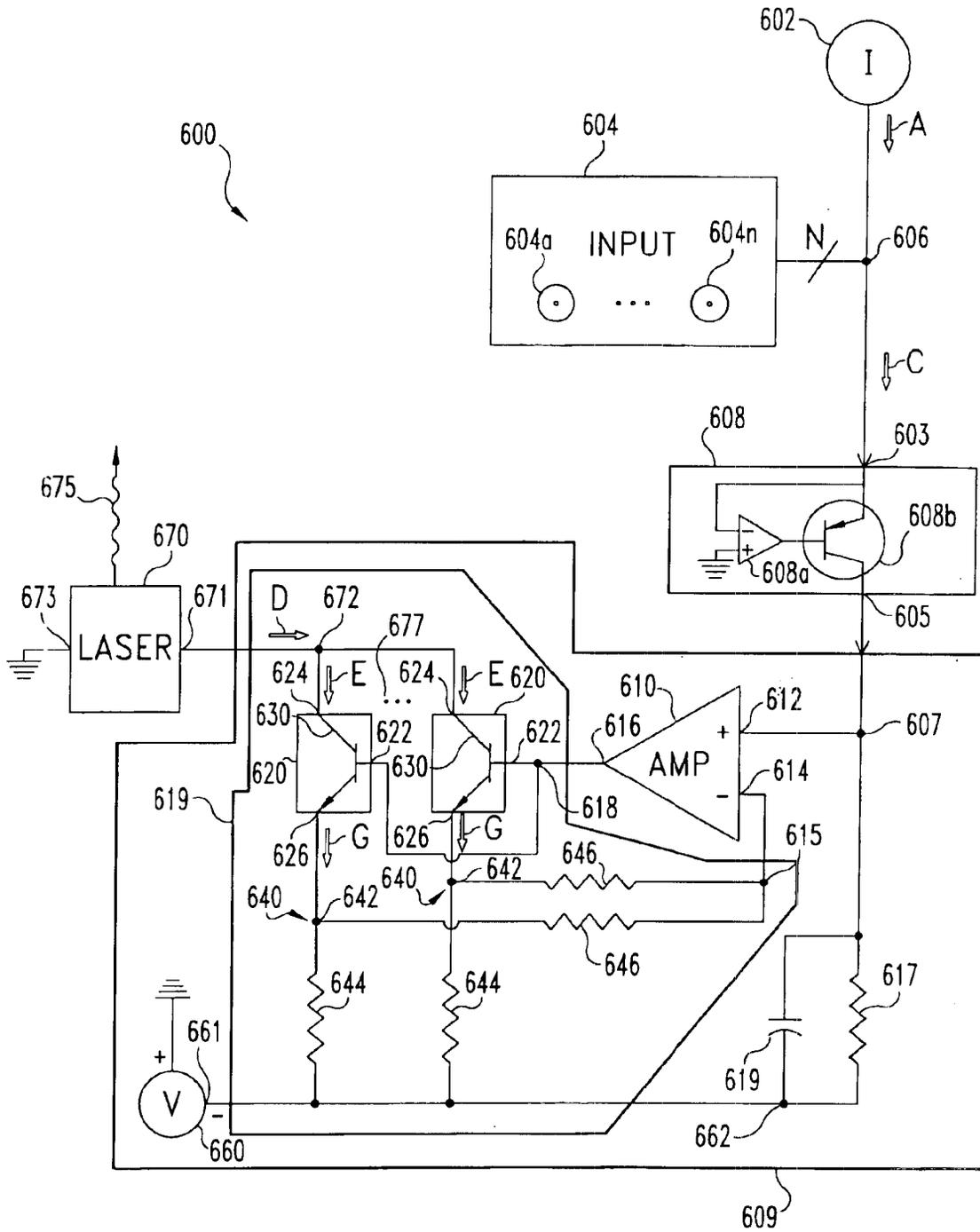


Fig. 6

CURRENT CONTROL CIRCUITRY**CROSS-REFERENCE TO RELATED APPLICATIONS**

The present application is a continuation-in-part of commonly owned U.S. patent application Ser. No. 09/964,953, filed Sep. 27, 2001 now U.S. Pat No. 6,696,887, which is hereby incorporated by reference in its entirety.

GOVERNMENT RIGHTS

This invention was made with Government support under Contract Number DE-AC0676RLO1831. The Government has certain rights in the invention.

BACKGROUND

The present invention relates to electrical circuitry, and more particularly, but not exclusively, relates to interface circuits including a transistor.

The ongoing desire for faster circuitry with fewer components has fueled a need for better ways to interface various circuits and circuitry components. Improved interfacing for electro-optical devices, such as photodetectors and laser generating components, is of particular interest. Proposed interface circuits for certain photodetectors typically limit the available frequency response and/or signal-to-noise ratio of such devices. In other proposed arrangements, interfaces between certain laser generating components and one or more corresponding input signal sources often include complicated filter networks in an attempt to provide adequate impedance matching. Besides electro-optics, other applications would also benefit from better interfacing. Thus, there is a demand for further advancement in this area of technology.

SUMMARY OF INVENTION

As used herein, "transistor device" broadly refers not only to a single transistor, but also to a transistor combined with one or more other electronic elements to provide an active device that includes at least three terminals. By way of nonlimiting example, transistor device includes multiple transistor combinations, such as two or more transistors connected in parallel, the Darlington configuration, and the Sziklai configuration, to name a few; or different configurations including at least one transistor as would occur to one skilled in the art. Further, as used herein, "transistor" broadly refers to any transistor type, including, but not limited to, a Bipolar Junction Transistor (BJT), Junction Field Effect Transistor (JFET), Insulated Gate Field Effect Transistor (IGFET) (where IGFETs include Metal Oxide Semiconductor Field Effect Transistor (MOSFET) types). Also as used herein, "common base" or "common gate" refers to a transistor device for which input and output signals of interest are each associated with a transistor device terminal other than a base or gate.

One embodiment of the present invention includes a unique interface circuit. Other embodiments include unique circuits, systems, devices, apparatus, and methods for interface circuitry.

In a further embodiment, interface circuitry includes a transistor device in a common base or common gate configuration. This configuration can include a servo device that receives feedback from one terminal of the transistor device to maintain a relatively constant level at that terminal.

Still a further embodiment of the present invention includes a transistor device in a common base or common

gate configuration that amplifies an input signal from a photodetector. A transistor emitter is coupled to the photodetector to receive the input signal and an output is provided from a transistor collector. An operational amplifier can be included with an output operable to drive a transistor base and a negative input coupled to the transistor emitter.

Yet another embodiment of the present invention includes: controlling operation of a transistor device in a common base or gate mode with a servo device; providing negative feedback from a first terminal of the transistor device to a first input of the servo device; providing a selected voltage level to a second input of the servo device; and biasing another device coupled to the first terminal in accordance with the selected voltage level.

Another embodiment of the present invention includes: operating a transistor device in a common base or gate configuration; coupling two or more input signal pathways to the transistor device; and providing an output to another device from the transistor device. In one form, this other device is of a laser-generating type.

For another embodiment, a transistor device includes an emitter, a base, and a collector, that is arranged in a common base configuration to maintain the emitter at a predefined voltage. A number of input signal pathways are coupled to the emitter and a current-dependent load is coupled to the collector. This load is responsive to an input signal received through one of the input signal pathways.

Circuitry of a further embodiment of the present invention includes a transistor device operated in a common base or gate configuration to provide a virtual ground at a first terminal, and a laser device electrically coupled to a second terminal of the transistor device. Operation of the laser device is controlled with one or more input signals provided to the first terminal of the transistor.

Yet a further embodiment of the present invention includes: controlling a common base or common gate mode of operation of a transistor device with a servo device, where the servo device provides an output to the transistor device and receives feedback from the transistor device; applying an approximately constant bias current to the transistor device with a current source; receiving an input signal at an electrical node between a first terminal of the transistor device and the current source; and providing an output signal from the transistor device.

Another embodiment of the present invention includes: providing an amplifier including two inputs and an output; providing a voltage to one of the inputs; providing a number of semiconductor devices each including three terminals one of which is a base or gate; operatively connecting the amplifier output to the base or gate of each of the semiconductor devices effective to provide a current from the first terminal of each of the semiconductor devices; and providing the current of each of the semiconductor devices to a laser effective to control operation of the laser.

Circuitry according to a further embodiment of the present invention includes: a differential amplifier including a positive input, a negative input, and an output; a number of semiconductor devices each including a base or gate operatively connected to the output of the amplifier and two other terminals, a feedback network operatively connecting one of the two other nodes of each of the semiconductor devices to the negative input of the amplifier; and a current-controlled laser device operatively connected to the other of the two other nodes of each of the semiconductor devices.

Yet a further embodiment of the present invention includes: operating an amplifier including an input and an

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output; providing a number of transistors each including a base or gate responsive to the output of the amplifier, and two other terminals; changing an input signal input of the amplifier; for each of the transistors, adjusting current flow through the two other terminals with the amplifier according to such change; and controlling operation of a laser device with the current flow.

One object of the present invention is to provide a unique interface circuit.

Another object of the present invention is to provide a unique interface circuit, system, device, apparatus, or method.

A further object of the present invention is to provide a unique laser driving circuit, apparatus, or method.

Other objects, embodiments, forms, features, advantages, benefits, and aspects of the present invention shall become apparent from the detailed description and drawings provided herewith.

BRIEF DESCRIPTION OF DRAWINGS

FIG. 1 is a schematic of an interface circuit of one embodiment of the present invention.

FIG. 2 is a schematic of circuitry of another embodiment of the present invention arranged to interface one or more signals with an electrical load.

FIG. 3 is a schematic showing greater detail of one form of the embodiment shown in FIG. 2.

FIG. 4 is a schematic illustrating interface circuitry of still another embodiment of the present invention.

FIG. 5 is a schematic illustrating interface circuitry for a photodiode sensor of yet another embodiment of the present invention.

FIG. 6 is a schematic illustrating laser driving circuitry of another embodiment of the present invention.

DETAILED DESCRIPTION OF SELECTED EMBODIMENTS

While the present invention may be embodied in many different forms, for the purpose of promoting an understanding of the principles of the invention, reference will now be made to the embodiments illustrated in the drawings and specific language will be used to describe the same. It will nevertheless be understood that no limitation of the scope of the invention is thereby intended. Any alterations and further modifications in the described embodiments, and any further applications of the principles of the invention as described herein are contemplated as would normally occur to one skilled in the art to which the invention relates.

FIG. 1 schematically illustrates circuit 20 of one embodiment of the present invention. Circuit 20 includes interface circuitry 22, input (I/P) signal source 24 operable to provide an input signal to interface circuitry 22, and output (O/P) circuitry 26 responsive to an output signal from interface circuitry 22. Interface circuitry 22 changes various characteristics of the input signal for output to O/P circuitry 26 as compared to the provision of this input signal to O/P circuitry 26 directly from I/P signal source 24. Interface circuitry 22 includes transistor device 30, servo device 40, current source 50, and voltage source 60. Signal source 24, transistor device 30, servo device 40, and current source 50 are electrically coupled at a common input node 70 of interface circuitry 22.

Transistor device 30 is in the form of NPN bipolar junction transistor 31. Transistor 31 includes base 32b

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electrically coupled to servo device 40, collector 32c electrically coupled to O/P circuitry 26, and emitter 32e electrically coupled to input node 70. Servo device 40 includes operational amplifier (op-amp) 41 with negative op-amp input 42, positive op-amp input 44, and op-amp output 46. Negative op-amp input 42 is electrically coupled to emitter 32e and signal I/P source 24 via input node 70. Positive op-amp input 44 is electrically coupled to voltage source 60, and op-amp output 46 is electrically coupled to base 32b of transistor 31.

Current source 50 is coupled to a voltage supply (-V) that is negative relative to electrical ground. Current source 50 provides an approximately constant current with compliance suitable to the particular application. Current source 50 can be arranged to permit for adjustment of the output current level by an operator or otherwise, or can be of a fixed, nonadjustable output variety.

Input signal source 24, output circuitry 26, and voltage source 60 are commonly grounded. Voltage source 60 provides a voltage level to positive op-amp input 44 that is positive relative to electrical ground. Voltage source 60 provides an approximately constant voltage output with a degree of regulation suitable for the particular application. Voltage source 60 can be arranged to permit for adjustment of the output voltage level by an operator or otherwise, or can be of a fixed, nonadjustable variety.

During operation, an I/P signal from I/P signal source 24 is applied to emitter 32e and a corresponding O/P signal is provided to O/P circuitry 26 from collector 32c. Transistor device 30 and servo device 40 are configured to operate in a common base mode such that base 32b remains a generally common reference point relative to the I/P signal at emitter 32e and the O/P signal at collector 32c. Operational amplifier 41 adjusts op-amp output 46, and correspondingly drives base 32b to maintain the voltage difference between negative op-amp input 42 and positive op-amp input 44 close to zero. Accordingly, negative op-amp input 42 receives negative feedback from emitter 32e, resulting in a voltage level at emitter 32e corresponding to that provided to positive op-amp input 44 by voltage source 60. Furthermore, it should be understood that the electric current drawn by negative op-amp input 42 is relatively low compared to the current flow from emitter 32e to collector 32c of transistor device 30. Current source 50 provides an appropriate bias current to maintain transistor device 30 in a generally linear conductive range for the common base configuration.

The common base mode of operation provides a way to isolate reactance characteristics of signal source 24 from O/P circuitry 26 and corresponding provide impedance matching/transformation. Typically, the input impedance of emitter 32e is significantly lower than the output impedance of collector 32c, which can be desirable for high frequency input signal conditioning and/or signal amplification, among others. When voltage source 60 provides a nonzero voltage to positive op-amp input 44, node 70 is maintained at a comparable nonzero voltage. This voltage can be used to bias certain passive forms of signal source 40, such as a sensor or detector. In another arrangement, voltage source 60 can be at a zero level relative to ground, which could be alternatively represented by an electrical short or resistance connection from positive op-amp input 44 to electrical ground. For this arrangement, operational amplifier 41 acts to make the voltage difference between positive op-amp input 44 and negative op-amp input 42 approach zero, so that a virtual ground is realized at input node 70. This virtual ground arrangement can be used to provide control signals from I/P signal source 24 to O/P circuitry 26 while isolating undesirable characteristics of I/P signal source 24 from O/P circuitry 26.

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FIG. 2 schematically illustrates circuit 120 of another embodiment of the present invention. Circuit 120 includes interface circuit 122, input signal sources I/P1, I/P2, . . . I/Pn (collectively designated I/P devices 124), and output circuitry 126. Input devices 124 provide signals to output circuitry 126 via interface circuit 122. Input devices 124 can provide two or more signals simultaneously, such that they are summed together by interface circuit 122, or signals may be provided by different input devices 124 at different times. While three input devices 124 are shown in FIG. 2, it should be understood that the ellipse positioned between I/P2 and I/Pn represents the optional presence of more input devices 124. In still other embodiments, two or less input devices 124 could be utilized.

Interface circuit 122 includes transistor device 130 and servo device 140 configured for a common base mode of operation. Transistor device 130 includes PNP transistor 131 with base 132b, collector 132c, and emitter 132e. Servo device 140 includes operational amplifier 141 with negative op-amp input 142 coupled to input node 170 and, positive op-amp input 144 at ground. Operational amplifier 141 also includes op-amp output 146 electrically coupled to base 132b of transistor device 130 via low pass (LP) filter 148. Transistor device 130 and servo device 140 operate as described in connection with transistor device 30 and servo device 40 of FIG. 1. It should be appreciated that positive op-amp input 144 could be tied directly to ground as illustrated, or through a resistor as is commonly desired for many operational amplifier devices. Likewise, low pass filter 148 may not be present, but it can be desired for certain applications to reduce noise and provide a smoother response.

Input node 170 is common to emitter 132e and input resistors R1, R2, . . . Rn. As illustrated, input node 170 is directly connected to negative op-amp input 142; however, in other embodiments, an interfacing passive component, such as a resistor, or network of passive components could be used to couple input node 170 to negative op-amp input 142. Bias current is applied to transistor device 130 by current source 150 via a noise reducing low pass filter 152 coupled in series with current source 150. Current source 150 provides an approximately constant current with a degree of compliance suitable to the particular application. Current source 150 can be arranged to permit adjustment of output current by an operator or otherwise, or can be of a nonadjustable, fixed variety.

O/P circuitry 126 includes current source 180 and load (Z) 190. Load 190 includes laser generating device 192. In one form, laser device is of a current-dependent load type, such as a quantum cascade laser, and current source 180 is of a variable type arranged as the main current drive for load 190. In other embodiments, load 190 and device 192 may be in the form of a laser diode or other laser generator, and/or include a different type of load.

The coupling of positive op-amp input 144 to ground provides a virtual ground at negative op-amp input 142 and correspondingly input node 170. Signals input to emitter 132e from any of input devices 124 are combined and output to load 190 from collector 132c. The isolation characteristic of interface circuit 122 permits the combination of signals operating with different voltage supply rails—such that amplifiers operating off a plus/minus five (+/-5) volt supply can be combined with those operating off a plus/minus fifteen (+/-15) volt supply. Additionally or alternatively, high frequency modulation signals for load 190 can be added to other relatively slow-changing control signals without the need for complex interfacing filter networks.

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FIG. 3 schematically illustrates circuitry 220 of another embodiment of the present invention. Circuitry 220 includes interface circuit 222, two (2) inputs VIN1 and VIN2 (collectively designated inputs 224) that are supplied by sources not shown, and output circuitry 226. Inputs 224 can be provided by devices such as devices 124 of circuit 120. Interface circuit 222 includes transistor device 230 and servo device 240 arranged for a common base mode of operation as previously described for transistor device 30 and 130 and servo device 40 and 140 for circuit 20 and 120, respectively. Interface circuit 222 further includes current source circuit 250 and low pass filter circuit 252 coupled in series to input node 270. Input node 270 is electrically connected to the emitter of transistor device 230 and coupled to the negative input of servo device 240. The positive input of servo device 240 is tied to ground via a resistor to provide a virtual ground at input node 270 as explained in connection with circuit 120. Input VIN1 is coupled to input node 270 via filter 272 and input VIN2 is coupled to input node 270 via an input resistor.

Output circuitry 226 includes a current source circuit 280 and load output VOUT (also designated by reference numeral 290). The output from the collector of transistor device 230 is provided to current source circuitry 280, which in turn, provides output VOUT to an electrical load (not shown). In one form, VOUT drives a laser device of a current-dependent variety, such as a quantum cascade type. For this form, current source circuitry 280 is arranged to provide a desired load current that is modulated/controlled by signals from inputs 224. VIN1 can be a 0–20 control voltage and VIN2 can be provided as a sweep signal for such a form. This arrangement provides for the input of control signals at inputs 224, while isolating undesirable reactance characteristics from the load coupled to output VOUT.

FIG. 4 schematically illustrates circuitry 320 of yet another embodiment of the present invention. Circuitry 320 includes interface circuit 322; inputs 324 (individually designated as VIN1, VIN2, . . . VINn); and output circuitry 326. Inputs 324 each provide a signal to output circuitry 326 via interface circuit 322, and can be supplied by one or more input sources or devices (not shown). Signals from two or more inputs 324 received simultaneously can be summed together with interface circuit 322. While three inputs 324 are shown in FIG. 4, it should be understood that the vertical ellipse positioned between VIN2 and VINn represents the optional presence of more inputs 324. In other embodiments, two or less inputs 324 can be utilized.

Interface circuit 322 includes PNP transistor 330 arranged for a common base mode of operation in conjunction with PNP transistor 340. Transistor 330 includes base 332b, collector 332c, and emitter 332e. Transistor 340 includes base 342b and collector 342c electrically coupled together with base 332b of transistor 330. Transistor 340 also includes emitter 342e coupled to ground. Bases 332b and 342b, and collector 342c are commonly coupled to biasing current source 360. Current source 360 is arranged to provide a biasing current for the operation of the transistor 340. The interconnection of transistors 330 and 340 maintains input node 370 of interface circuitry 322 (and correspondingly emitter 332e) at about the same electrical potential as emitter 342e, except for differences that might arise due to different transistor sizes, collector currents, junction temperatures, and the like. Accordingly, a virtual ground is approximated at input node 370 by this arrangement.

Input node 370 is also coupled to input resistors R1, R2, . . . , Rn; and a noise reducing low pass filter 352. Low pass filter 352 is coupled in series with biasing current

source **350**. Current sources **350** and **360** each provide an approximately constant biasing current with a degree of compliance suitable to the particular application. Current source **350** and/or current source **360** can be arranged to permit adjustment of output current by an operator or otherwise, or can be of a nonadjustable, fixed variety.

Output circuitry **326** includes current source **380** and load (**Z**) **390**. Load **390** can be of a current-dependent type, such as a quantum cascade laser, a different laser generating arrangement, and/or a different load type as would occur to those skilled in the art. The coupling of load **390** to inputs **324** via the virtual ground provided by interface circuit **322** provides a way to interface dissimilar signals, isolate undesirable electrical characteristics, such as reactance, and/or match/convert impedance of desired signals. In one form, circuitry **320** is applied to interface a current-dependent laser generating device with control/modulation signals provided from one or more other devices via inputs **324**.

Still another embodiment of the present invention is schematically illustrated as circuitry **420** in FIG. 5. Circuitry **420** includes preamplifier **422** for photodetector **424** and output circuit **426**. Photodetector **424** provides a signal indicative of a level of impinging photons. In one form, photodetector **424** is of Mercury-Cadmium-Telluride (MCT) type used for long-wave infrared wavelength detection. In other forms, photodetector **424** can be of a different type, including, but not limited to a silicon-based sensor typically used to detect visible light, an Indium-Galium-Arsenide (InGaAs) type often used for near infrared detection, or such different type as would occur to those skilled in the art. Interface circuitry **422** includes transistor device **430** coupled to servo device **440** to operate in the fashion previously described in connection with transistor device **30** and servo device **40** of circuit **20**. Transistor device **430** includes PNP transistor **431** with base **432b**, collector **432c**, and emitter **432e**. Servo device **440** includes operational amplifier **441** with negative op-amp input **442**, positive op-amp input **444**, and op-amp output **446**. Op-amp output **446** is coupled to low pass filter **448** to drive base **432b** of transistor **431**. Emitter **432e** is coupled to input node **470** to receive input signals from photodetector **424**. Input node **470** is also coupled to negative op-amp input **442** to provide negative feedback. Positive op-amp input **444** is coupled to variable voltage source **460**. Current source **450** and low pass filter **452** are coupled in series to input node **470** to provide an approximately constant biasing current to the transistor device **430** with a current source compliance suitable for the particular application. As explained in connection with circuit **20**, op-amp **441** operates to maintain voltage at node **470** at a value close to that input at positive op-amp input **444** by voltage source **460**. Accordingly, voltage source **460** can be adjusted to provide a desired biasing voltage level for photodetector **424**. Voltage source **460** is regulated to a degree suitable for the particular application and, like current source **450**, can be of a type that is adjustable by an operator or otherwise, or has a nonadjustable, fixed output.

Collector **432c** provides an amplified output of the input from photodetector **424** to emitter **432e**. Output circuitry **426** also includes Alternating Current (AC) amplifier **427a**. An AC voltage input to AC amplifier **427a** develops across inductor **428**, while also providing for the flow of an appropriate bias current to transistor device **430**. AC amplifier **427a** of circuit **426** provides an AC output, VACOUT. Output circuitry **426** also includes Direct Current (DC) amplifier **427b** and sense resistor **429**. The input to DC amplifier **427b** is developed across sense resistor **429**, such

that measurement of the current flow comprised of the bias current through transistor device **430** in addition to the current presented by photodetector **424** can be amplified. DC amplifier **427b** of circuit **426** provides a DC output, VDCOUT. Signals VACOUT and/or VDCOUT can be further utilized by communications circuitry, sensing circuitry, or such different applications as would occur to those skilled in the art.

The arrangement of circuit **420** provides a bias voltage for photodetector **424** while at the same time isolating its reactance characteristics from output circuitry **426**. In one form, photodetector **424** is of an MCT variety and amplifier **427a** is of a high frequency, high performance 50 ohm strip line amplifier type such that performance is comparable to the intrinsic capacitance of photodetector **424**. This arrangement finds application in digital communications, among others. In other embodiments, a different type of photodetector and/or amplifier could be utilized. In still other embodiments, a sensor for detecting electromagnetic radiation other than light, a substance, and/or a different property/characteristic could be used instead of or in addition to photodetector **424**.

FIG. 6 schematically illustrates circuit **600** of another embodiment of the present invention. Circuit **600** includes a variable current source **602** which is adjusted to provide an approximately constant reference current. Current source **602** is selected to provide a suitable compliance and signal-to-noise ratio as would be desired for the particular application. Current source **602** is depicted with current flowing in the direction of arrow **A**; however, it should be understood that in other embodiments of the present invention, such as those with opposite polarities, current flow could be in the opposite direction. Circuit **600** further includes input devices **604** with signal input **604a** and signal input **604n**. The signal inputs of circuit **604** receive input signals that may be of a variety of frequencies. Furthermore, two or more input signals may be provided simultaneously or input signals may be provided to the signal inputs of circuit **604** at different times. It should be understood that the ellipse positioned between signal inputs **604a** and **604n** represents the optional presence of additional signal inputs. In still other embodiments a single signal input could be used.

Inputs **604a**, **604n** from devices **604** are coupled together at electrical node **606**. Furthermore, as designated by a slash, **N** number of input signals can be coupled at node **606**. All input signals from input devices **604** and the reference current from current source **602** are provided to input **603** of cascode combiner **608**. Cascode combiner **608** also includes output **605** coupled to current control circuit **609**. Cascode combiner **608** matches impedance of devices **604** via input **603** to circuit **609** via output **605**. Cascode combiner **608** includes servo device **608a** coupled to transistor device **608b** in a common base configuration like that of servo **140** and transistor device **130**, respectively, as described in connection with interface circuit **122** of FIG. 2; however, LP filter **148** of circuit **122** is not explicitly depicted. Indeed, LP filter **148** could likewise be included in cascode combiner **608**.

FIG. 3 illustrates another implementation that could be used for cascode combiner **608**, in which node **270** of interface circuit **222** corresponds to node **606** of FIG. 6, servo device **240** corresponds to servo device **608a**, transistor **230** corresponds to transistor device **608b**, and the output from the collector of device **230** corresponds to output **605**. Some or all of the passive components coupled between servo device **240** and transistor **230** shown in FIG. 3 can likewise be included in cascode combiner **608** to provide signal conditioning/filtering as desired. Cascode combiner

608 could alternatively be provided by the arrangement of transistors 330 and 340 included in interface circuitry 322 of FIG. 4 with node 606 corresponding to node 370 and output 605 corresponding to the output of interface circuitry 322 from collector 332c. Likewise for situations of an opposite relative voltage polarity, the opposite type of common base transistor (i.e. NPN) can be utilized as presented in the FIG. 1 arrangement. Alternative or additionally, an FET can be used in place of a transistor in providing cascode combiner 608.

Output 605 of cascode combiner 608 is provided as an input to current control circuit 609. Circuit 609 is arranged to regulate current flow through laser device 670. Circuit 609 includes resistor 617, capacitor 619, and output 605 coupled at node 607. Resistor 617 and filter capacitor 619 are also coupled to node 662 which is in turn coupled to output 661 of voltage source 660 that is negative relative to electrical ground. Resistor 617 provides a voltage difference between node 662 and node 607 that can serve as a reference voltage. Capacitor 619 has an impedance that is smaller than that conventionally associated with filter capacitors of circuits for driving current-dependent lasers. This arrangement permits voltage variation at relatively greater speeds and magnitudes.

Differential amplifier 610 includes positive input 612 common to node 607, negative input 614, and output 616. In operation, the signal provided by output 616 of differential amplifier 610 is proportional to the difference between its positive input 612 and negative input 614. The output of differential amplifier 610 is operatively coupled to current regulating subcircuit 619 at node 618. It should be understood that differential amplifier 610 may take various forms including, for example, an operational amplifier or a combination of integrated circuit components.

Subcircuit 619 includes a number of semiconductor devices 620 each in the form of transistor device 630 that are provided in an electrically parallel arrangement. As depicted in FIG. 6, transistor devices 630 are each shown as a NPN type including a base, collector, and emitter corresponding to terminals 622, 624, and 626, respectively. Each terminal 622 (base) is commonly coupled at node 618 and each terminal 624 (collector) is commonly coupled at node 672.

Each terminal 626 (emitter) is coupled to a resistive network 640 at as respective node 642. Each network 640 includes resistor 644 coupled to negative output 661 of voltage source 660 at node 662 and resistor 646 coupled to negative input 614 of amplifier 610 at node 615 to provide corresponding feedback. Furthermore, as indicated by ellipse 677, it is contemplated that subcircuit 619 may include additional semiconductor devices 620 arranged in parallel with corresponding resistor networks 640. For example, circuit 600 may include six or more NPN bipolar junction transistors in a parallel configuration, or such other semiconductor device types as appropriate to the particular application, and may be of differing types in the same subcircuit.

Laser 670 includes current-control input 671 coupled to subcircuit 619 at node 672 with a corresponding electrical ground at terminal 673. In operation, laser 670 emits light symbolically designated by reference numeral 675 at one or more selected wavelengths and/or frequencies. Laser 670 may be any type of current-controlled laser, such as, for example, a quantum cascade laser or a diode laser.

During operation of circuit 600, a voltage is present at output 616 of amplifier 610. A corresponding voltage is also present at node 618 and applied to terminals 622 of semi-

conductor devices 620. The voltage applied to terminals 622 causes electrical current flow to terminals 624 in the direction of arrows E and causes corresponding electrical current flow from terminals 626 as indicated by arrows G. While the electrical current flowing through terminals 626 is typically larger than that flowing through terminals 624 due to base current flow, it should be understood that the relative magnitude of current flow between terminals 624 and 626 of each device 620 is controlled by the electrical input at terminals 622. Accordingly, variations in the voltage applied to terminals 622 over a typical operating range results in variations of the currents through terminals 624 and 626, respectively.

The electrical currents flowing through devices 620 result in a corresponding electrical current flow from laser 670 through node 672 in the direction of arrow D. This current flow can be controlled with circuit 609 to vary one or more characteristics of light 675 emitted by laser 670. It should be understood that variations in the currents flowing through devices 620 result in corresponding variation in the current flow through node 672. Thus, the output of amplifier 610 can be used to modulate the current flow through terminals 624 and 626 and correspondingly node 672, and, in turn, control the emission of laser light 675.

Operation of circuit 600 also results in voltage differentials across resistors 644 and corresponding voltages at nodes 642. Resistors 652 and 653 have electrical resistances that are larger than one typical for standard current-dependent laser control circuitry. This results in larger than normal voltages at nodes 642. It should be understood that the voltages at nodes 642 result in a corresponding voltage at node 615 that is also present at negative input 614. As previously stated, the output of amplifier 610 corresponds to the difference across positive input 612 and negative input 614. Furthermore, the voltages at nodes 640 vary with the operation of devices 620. Thus, circuit 600 utilizes feedback from semiconductor devices 620 to stabilize and regulate electric current flow through laser device 670. This current flow can be changed in response to changes input to devices 604 via cascode combiner 608.

Referring generally to FIGS. 1-6, it should be appreciated that in other embodiments, instead of a single transistor, a transistor device that combines one or more transistors and/or one or more other active devices, including combinations of two or more transistors, could be utilized. Furthermore, it should be understood that in place of one or more Bipolar Junction Transistor (BJT) types illustrated, a Field Effect Transistor (FET) type could be utilized. In such a case, an FET gate is used instead of the base, an FET source is used in place of one of the collector and emitter, and an FET drain is used in place of the other of the collector and emitter. For embodiments utilizing a BJT-based transistor device in a common base configuration or operating mode, and FET-based transistor device can be used instead of the BJT-based transistor device, with such FET-based transistor device correspondingly being in a common gate configuration or operating mode.

In still other forms, a servo device utilized in accordance with the present invention may alternatively or additionally include other types of circuit arrangements in addition to or as an alternative to an operational amplifier. In one such example, a differential input amplifier of discrete components is utilized in place of an operation amplifier. Moreover, in yet other embodiments, different applications and combinations of the circuits shown in the respective embodiments are utilized.

It should be understood that any devices, transistors, op-amps, servos, circuits, subcircuits, and/or other compo-

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nents described herein, and any inputs, outputs, nodes or terminals described in connections therewith can be provided as discrete components, integrated with one another as integrated circuitry, hybrid circuitry, or a combination thereof. Further, as used herein "terminal" and "node" refer broadly to any connection point of the corresponding component, device, or circuit and could be internal to embodiments provided as integrated circuitry, hybrid circuitry, and the like.

Any theory, mechanism of operation, proof, or finding stated herein is meant to further enhance understanding of the present invention, and is not intended to limit the present invention in any way to such theory, mechanism of operation, proof, or finding. While the invention has been illustrated and described in detail in the drawings and foregoing description, the same is to be considered as illustrative and not restrictive in character, it being understood that only selected embodiments have been shown and described and that all equivalents, changes, and modifications that come within the spirit of the inventions as defined herein or by the following claims are desired to be protected.

What is claim is:

1. A method comprising:

providing an amplifier including a first input, a second input, and an output; providing a cascode combiner with a virtual ground node to combine a number of different input signals in an output signal and isolate undesirable reactance characteristics of the input signals from the output signal;

operatively coupling the cascode combiner to the amplifier to provide the output signal to the first input; and

providing a plurality of semiconductor devices each including a first terminal, a second terminal, and a third terminal, the third terminal being a base or gate, the first terminal of each of said semiconductor devices being coupled together to form a common node;

connecting a laser to the common node, the common node being between said laser and said first terminal of each of said semiconductor devices;

directly connecting the output of said amplifier to the base or gate of each of said semiconductor devices; and

controlling current flowing through the laser and the common node with said amplifier and said semiconductor devices, the current flow being divided to pass through the first terminal of each of said semiconductor devices.

2. The method of claim 1 further comprising providing feedback from the second terminal of each of said semiconductor devices to the second input of said amplifier.

3. The method of claim 2 wherein the amplifier is a differential amplifier, the semiconductor devices are transistors, and the laser is one of a diode laser and a quantum cascade laser.

4. The method of claim 1 wherein the semiconductor devices are transistors, and the semiconductor devices are connected together in parallel to one another.

5. The method of claim 4 wherein the base or gate of each of said semiconductor devices are directly coupled together

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to form another common node and the amplifier includes at least one passive component directly coupled to the common node.

6. The method of claim 1 wherein said laser is one of a diode laser and a quantum cascade laser.

7. A circuit comprising:

a differential amplifier including a positive input, a negative input, and an output;

a cascode combiner including amplifier circuitry, a transistor, a number of cascode combiner inputs and a cascode combiner output, the transistor including a transistor base or a transistor gate and two nodes, the amplifier circuitry including an amplifier output and two inputs, the amplifier output being connected to the transistor base or transistor gate, a first one of the two inputs being coupled to ground, a second one of the two inputs being coupled to a first one of the two nodes to provide a virtual ground coupled together with the cascode combiner inputs, a second one of the two nodes providing the cascode combiner output coupled to the positive input of the differential amplifier to provide a combination of the cascode combiner inputs to the differential amplifier with isolation of undesirable reactance characteristics;

a plurality of semiconductor devices each including a base or gate and two other nodes, the base or gate of each of said plurality of semiconductor devices being operatively connected to the output of said amplifier, said plurality of semiconductor device being coupled in parallel to one another;

a feedback network operatively connecting a first one of the two other nodes of each of said plurality of semiconductor devices to the negative input of said amplifier; and

a current-controlled laser device operatively connected to a second one of the two other nodes of each of said plurality of semiconductor devices, the second one of the two other nodes of each of said plurality of semiconductor devices being directly coupled together to form a first common node, the first common node being positioned between said laser device and said plurality of semiconductor devices.

8. The circuit of claim 7 wherein the feedback network includes a plurality of resistor providing a voltage differential between the first one of the two other nodes and the negative input of said amplifier.

9. The circuit of claim 7 wherein the laser device is one of a diode laser and a quantum cascade laser.

10. The circuit of claim 7 wherein the semiconductor devices each include an FET transistor or a bipolar junction transistor, and the base or gate of each of said plurality of said semiconductor devices are coupled together to form a second common node.

11. The circuit of claim 10 wherein the second common node is positioned between the output of the amplifier and the semiconductor devices.

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